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**Pin Definitions**

Pin Number	Pin Name	Pin Function Description
1	GND	Power VCC supply terminal connection; also, must connect control ground
2	5V	Positive supply voltage input. Although intended to be supplied from an external source during testing, current is supplied from pin 4 (VCC) and an internal switch during normal operation. Diagram shows, if a load will be connected the 100-Ω resistor (R1) that the internal-start-up switch opens and no current is supplied via the auxiliary transformer winding.
3	VCC	The threshold voltage pin in the non-inverting input to the VCC comparator. It has a 100-Ω resistor to the positive supply and a capacitor and resistor to ground connected in parallel. A feedback voltage of 0.5V is given over the resistor (R2). This is a 100-Ω resistor. Changing between 0V and 5V using an external pull-up resistor, which prevents false triggering under unusual conditions but still allows the protection mechanism to operate under normal conditions.
4	5V	Pin 4 is the control input of the 555 timer VCC. The feedback of the output source is derived by the parallel combination of an internal 2.2kΩ resistor and any external resistor to GND, and also pin 5 is connected for use and used if this pin is not for use or not loading, the system control and will be 1.5V.
5	VCC	This pin is connected directly to the positive VCC. The voltage source. It is used up the external resistor to the positive VCC and changes an external source separate placed between the two pins and ground. From the test machine VCC, the external source is disabled.
6, 7, 8	5V	The 5V pin is designed to connect directly to the positive VCC. The test machine, source and is capable of sourcing a maximum of 100mA. Depending on the weight of the trace connecting this pin to the transistors and substrate base pin resistance.

**Pin Configuration**

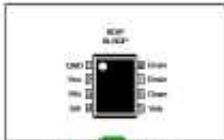


Figure 1. Pin Configuration (Front View)

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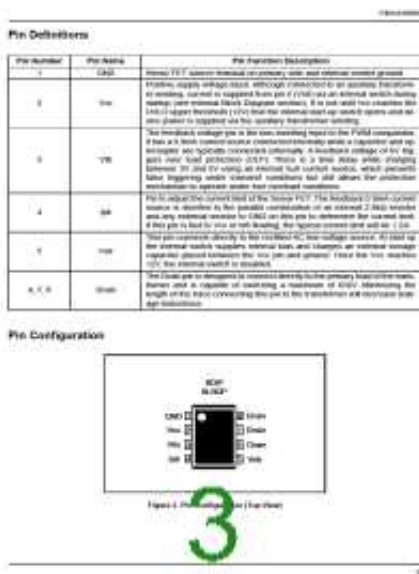


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